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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

MAY 31 2006

Group Art Unit: 2814
Examiner: Howard Weiss
Confirmation No.: 8511

In Re PATENT APPLICATION OF:

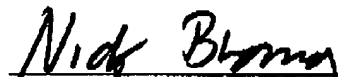
Applicant: Kiyonori Watanabe)
Serial No.: 10/697,318)
Filed: October 31, 2003) INTERVIEW SUMMARY
For: SEMICONDUCTOR DEVICE WITH)
IMPROVED DESIGN FREEDOM OF)
EXTERNAL TERMINALS)
Attny Ref.: OHG 144)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The Applicant summarizes the interview of May 11, 2006 as follows: The case was discussed as noted by the Examiner on the Interview Summary.

Respectfully submitted,

May 31, 2006
Date
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